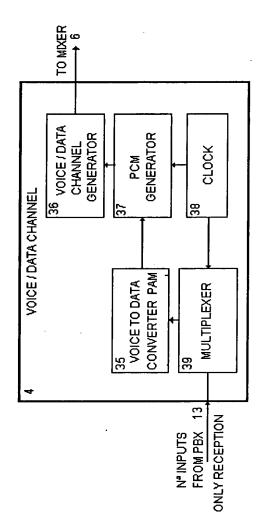
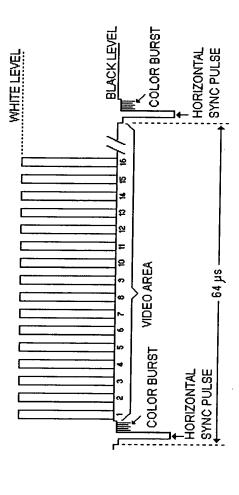


 $\frac{1}{\beta} \leq \varepsilon$ 

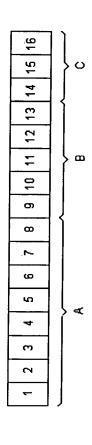
THE EQUIPMENT IS NORMALLY INTEGRATED IN CPU TERMINAL



INFORMATION FORMAT FOR EACH ONE OF THE 10 LINES IN FIRST PLACE



BITS PATTERN OF LINE 1



A = 8 BITS TO DEFINE EACH PCM UNITARY MODULE (FOR DATA)

B = 5 BITS FOR GENERAL INFORMATION. (UP TO 3 DIFFERENTS PAGES WITH INFORMATION) FIRST 3 BITS DEFINE THE FORMAT, NEXT 2 BITS DEFINE NUMBER OF PAGES

C = 3 BITS TO DEFINE NUMBER OF PAGES OF DATA CHANNEL

BITS SHAPE OF LINES 2 TO 10

A = MAXIMUN QUANTITY OF BITS PER LINE ACCORDANCE TO ACTUAL PROGRAM EVERY LINE IT IS USE FOR TO WRITE THE NUMBER OF CHANNELS, EACH ONE NUMBER DEFINE IF THE CHANNEL WILL BE OPEN, IS IT REQUESTED

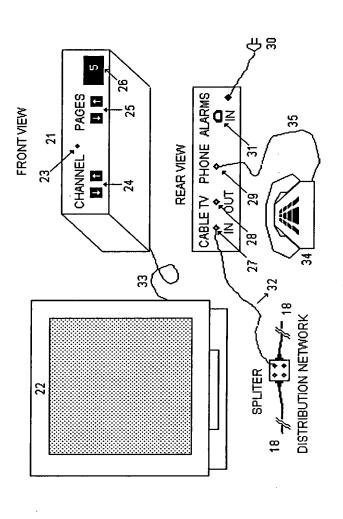
LINE 2 = LIST OF CHANNEL BASIC

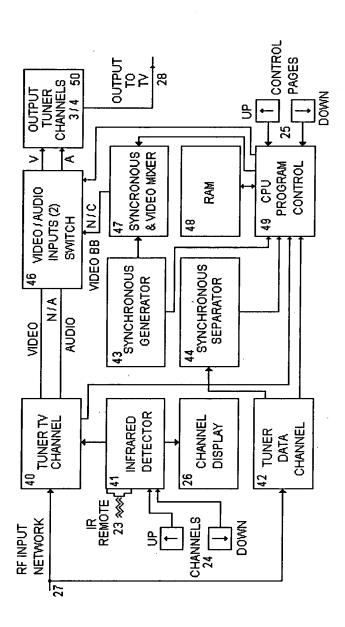
LINE 3 = LIST OF PPV CHANNELS

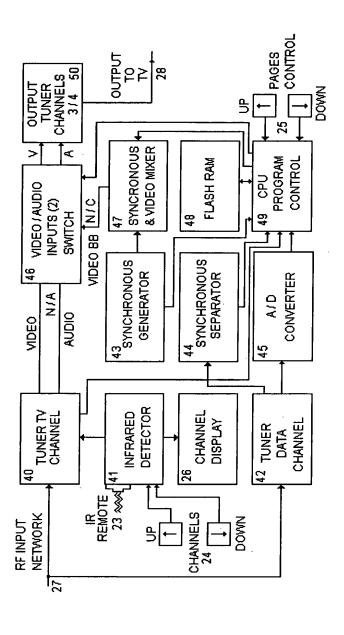
LINE 4 = LIST OF SPECIAL EVENTS CHANNELS

LINE 5 = LIST OF ADDITIONAL CHANNELS

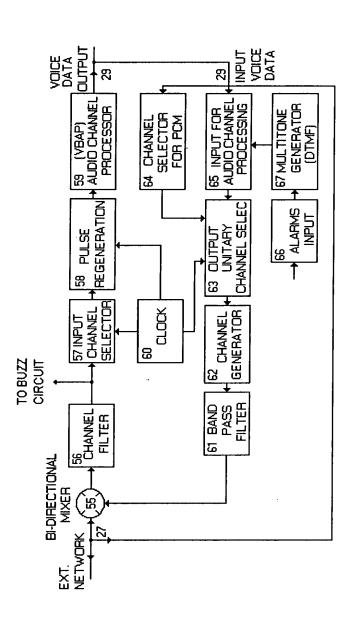
LINE 6 TO 10, ARE USE FOR TO SCRABBLE CHANNELS

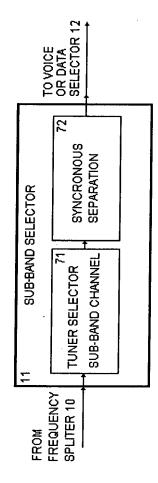


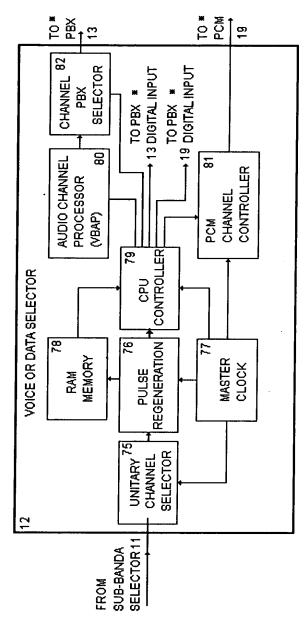




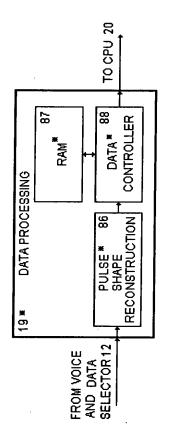
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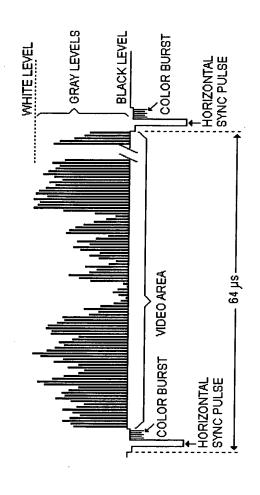




THIS OUTPUT DEPENDS ON CONFIGURATION OF THE EXTERNAL EQUIPMENT



\* THESE CIRCUITS WOULD BE PART OF CPU TERMINAL (20)



F-[	CMZZÞIU A CPC 200 − C	SP. A → BET 33
21	Fig. 7/8  46  49  48  RAM  Y R L  D R D  D R D  E L  D R D  HR RAM  MIX C  SWITCH  SYNC SEP  43	Fig. 9 41 INFRARED & CONTROL DISPLAY & CONTROL DISPLAY
	40 TUNNER CHANNEL 42 TUNNER DATA CHANNEL 50 CUTPUT TUNNER CHANNEL	POWER SUPLY LINE TRANSF D2 D3 CAP CAP REG
2	;	۶ <u> </u>

